

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q76734

Takanori SAEKI

Appln. No.: (Divisional of U.S. Serial  
No. 09/775,152, filed January 8, 2001 Confirmation No.: 6775)

Confirmation No. (not yet assigned)

Group Art Unit: Not yet assigned

Filed: July 28, 2003

Examiner: Not yet assigned

For: CLOCK CONTROL METHOD AND CIRCUIT

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 CFR §1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form which are all the references of record in parent application No. 09/755,152. Applicant is not submitting duplicate copies of these references but requests that they be listed on the face of any patent granted on the above application. (See 37 CFR §1.98(d)). Copies of any cited copending applications, if not previously submitted, are being submitted herewith.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not

INFORMATION DISCLOSURE STATEMENT  
Divisional of Application. No.: 09/755,152

waive any right to take any action that would be appropriate to antedate or otherwise remove any  
listed document as a competent reference against the claims of the present application.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'H. Bernstein', written over a horizontal line.

Howard L. Bernstein  
Registration No. 25,665

SUGHRUE MION, PLLC  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860

WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: July 28, 2003

Substitute for Form 1449 A & B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	DIV of 09/755,152
				Confirmation Number	Not yet assigned
				Filing Date	July 28, 2003
				First Named Inventor	Takanori SAEKI
				Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
Sheet	1	of	1	Attorney Docket Number	Q76734

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code <sup>2</sup> (if known)		
		US 6,184,736		02-2001	Wissell et al.
		US 6,137,336		10-2000	Baba et al.
		US 5,939,919		08-1999	Proebsting
		US 5,923,188		07-1999	Kametani et al.
		US 5,469,116		11-1995	Slemmer
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
		JP	8-237091		09-13-1996		
		JP	8-137091		05-13-1996		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
		J. Han et al., "Skew Minimization Techniques for 256M-bit Synchronous DRAM and Beyond", Symposium on VLSI Circuits Digest of Technical Papers (1998), pp. 192-193	
		R.B. Watson et al., "Clock Buffer Chip with Absolute Delay Regulation Over Process and Environmental Variations", Proc. of IEEE, Custom Integrated Circuits Conference (1992), pp. 25.2.1-25.2.5	
		Y. Okajima, et al., "Digital Delay Locked Loop and Design Technique for High Speed Synchronous Interface", IEICE Trans. Electron, Vol. E79-C, No. 6, June 1996, pp. 798-807	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kind Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov), MPEP 901.04 or in the comment box of this document. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to indicate here if English language Translation is attached.